台灣愛德萬教育訓練中心課程表 2020年7月 ~ 12月

SOC Test System – V93000 (SmarTest 7)

更新日期 2020/5/20

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 I-SMT7	V93000 SOC Digital User Training (SmarTest 7)	5	請洽教育訓練中心	無	7月06~10日 8月10~14日 10月19~23日 11月16~20日	Enable students to create semiconductor test programs on the V93000 SOC Test Platform. The intent is to provide the skills required to utilize the V93000 Tester Platform as an integral tool in the engineering and production flows of semiconductor device manufacturing. The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. After completion the student will be familiar with the following: (1) Key concepts and components of the V93000 (2) Setup basic Digital Test Programs (3) Pin configuration setup of Levels, Timing and Vectors (4) Calibration, Testflow, Test Methods, Debugging tools and concepts (5) DC Testing, Shmoo tools, Data logging, Histograms
V93000 MST-SMT7	V93000 SOC Mixed Signal User Training (SmarTest 7)	4	請洽教育訓練中心	*V93000 I-SMT7	7月14~17日 11月24~27日	After completing this training, the participant will be familiar with the use of instruments to test mixed signal performance parameters and specifications in the V93000 SOC Series. You will know the steps to: (1) Enable students to develop mixed signal device test programs on the V93000 SOC Test Platform. The intent is to provide the skills required to utilize the V93000 Tester Platform as an integral tool in the engineering and production flows of mixed signal device manufacturing. (2) The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. (3) Develop test programs for mixed-signal devices (4) Use the available tools for developing and debugging mixed-signal
V93000 PS RF	V93000 SOC Port Scale RF User Training (SmarTest 7)	5	請洽教育訓練中心	*V93000 I-SMT7 *V93000MST-SMT7	9月07~11日 12月07~11日	先修課程 Pre-requisites: The prerequisite for this class is the content of the 93000 Basic User Training class and the 93000 Mixed Signal Training class. It is expected that the students have a good understanding of RF fundamentals prior to attending this class. 課程目標 Course Object: The purpose of the 93000 Port Scale training series is to enable students to develop and use RF test programs based on 93000 Port Scale RF test system On completion of the course, participants: • know HW specification and structure of PSRF subsystem • know how to use software tool to create and measure a CW/Mod signal • know how to do RF calibration • know how to use RF API in Testmethod • know how to do an RF to BB setup & measurement
V93000 Production-SMT7	V93000 SOC Production Setup Training (SmarTest 7)	2	請洽教育訓練中心	*V93000 I-SMT7	9月29~30日 12月22~23日	The purpose of the V93000 SOC User Training for production setup is to enable students to operate and support production setup activities based on V93000 system. 適合對象: (此為初階課程, 適合新進人員) - Test Engineers - Product Test engineers - Production System Administrator On completion of the course, participants: (1) will be familiar with and know how to setup/configure production test environment including OS, Smartest and Licensing. (2) know how to create or maintain key components for enabling V93000 production including work-order, Prober/Handler driver and Application Model (3) know how to gather and analyze production data, including GDF,STDF,EDF (4) know the Test cell integration solution including Prober/Handler driver interface

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 TP	V93000 SOC Test Program Development Training (SmarTest 7)	4	請洽教育訓練中心	*V93000 I-SMT7 *V93000MST-SMT7	8月18~21日 12月15~18日	先修課程 Pre-requisites: V93000 Digital User Training, V93000 Mixed Signal Training, Others: C/C++ programming language 適合對象 Target Audience: Test engineer who needs to develop SOC or high speed test program 課程目標 Course Object: This course focus on test program development by using C/C++ style testmethod coding.The purpose of this course is to demonstrate the C/C++ example code by using testmethod API and standard C/C++ library, which includes DC Test, pattern creation, AC Test, high speed Test and Mixed-Signal Test. The TPD2.0 mainly emphasizes the PS1600 feature(sequence DC, TMU, high speed pattern sync and tracking).The TPD2.0 has to add the pattern conversion. So the TPD2.0 course can supply the whole 93k test program development from basic to mixed signal skill. (SMT Version: 7.2.2.4) Lecture: DC Test UTM introducion,93K DC resource overview ,FlexDC introduction ,DC set and Mset introduction, sequenced DC introduction, dynamic DC introduction, DC current profile introduction Lecture: Pattern creation Pattern conversion, vector label edit, D2S Lecture: AC Test Clock domain per pin introduction, frequency measurement(DCM), AC parameters measurement by TMU, HW PRBS generator and analyzer, Transition tracking, Clock keep alive Lecture: Mixed Signal Test ADC Distortion/Linearity, DAC Distortion/Linearity, SmartCalc(SMC) introduction, Sequencer burst, How to creat the library by UTM
MSF	Mixed Signal Fundamental	3	請洽教育訓練中心	*V93000 I-SMT7 *V93000MST-SMT7	10月13~15日	This training course is using 93K APIs to introduce mixed-signal testing fundamental. Compare with traditional text books(talked about "Signals & Systems"), this course creates a lot of sample programs to explain complicated phyiscal concepts, such as parameters of sinusoidal, sampling theroem, coherence condition, fast fourier transform. Let trainees refresh what they learned in a different perpestive, also enhance their capability to handle mixed signal testing challenges

SOC Test System – V93000 (SmarTest 8)

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 I-SMT8	V93000 SmarTest 8 Basic User Training	5	請洽教育訓練中心	建議熟悉 Fundamental Digital Testing	7月27~31日 9月21~25日 11月02~06日	Enable students to create semiconductor test programs on the V93000 Test Platform under SmarTest 8 software. The intent is to provide the skills required to utilize the V93000 Tester Platform as an integral tool in the engineering and production flows of semiconductor device manufacturing. The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. After completion the student will be familiar with the following: (1) Key concepts and components of the V93000 system (2) Understanding of the SmarTest8 SW concepts and how to use them (3) Setup of test programs using the SmarTest 8 features (4) The setups of Signals, Levels, Timing and Vectors (5) Operating Sequence, Testflow, Test Methods, Debugging tools and concepts (6) DC Testing, Shmoo tools, Data logging, Test tables, Utility lines

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 WS MX/RF	V93000 SmarTest 8 Wave Scale MX and RF User Training	3	請洽教育訓練中心	*V93000I-SMT8	8月04~06日 10月27~29日	先修課程 Pre-requisites: The prerequisite for this class is the content of the 93000 SmarTest8 Basic User Training class. It is expected that the students have a good understanding of RF and mixed signal fundamentals prior to attending this class. 適合對象 Target Audience: Test engineers or Product Test engineers who need to develop or support SmarTest8 Wave Scale MX and RF Test program on V93000. 課程目標 Course Object: The purpose of V93000 SmarTest8 Wave Scale MX and RF User Training series is to enable students to develop and use RF/Mixed Signal test programs based on V93000 SmarTest8 Wave Scale MX and RF test system. On completion of the course, participants: • Know specification, pogos, cabling, and interface of hardware in WSMX/RF. • Know how to use SMT8 software to create loopback measurement of WSMX/RF. • Know how to use device setup API in SMT8. • Know the calibration of WSMX/RF.
V93000 Production-SMT8	V93000 SmarTest 8 Production Integration User Training	2	請洽教育訓練中心	*V93000I-SMT8	9月02~03日 11月10~11日	Enable the user to use ST8 tools to setup a TestCell and run Production. 適合對象: - Test Engineers - Product Test engineers - Production System Administrator After completion the student will be familiar with the following: - SMT8 Test Program release to production, including process and usage of workspaces. - Test Cell control using TCCT for production engineering. - Setup of Test Cell for hand testing using TCCT Recipe setup. - Understand how Test Cell related variables can be setup to store production data and how Test Programs can have access to them. - Setting-up and simulate Test Cell for testing with a Handler or Prober in offline.

SOC Test System – V93000 Maintenance

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 PS H/W	V93000 Maintenance Training - Pin Scale (for SOC & HSM)	4	請洽教育訓練中心	無	9月14~17日 12月01~04日	This class introduces the principles of V93000 system maintenance and system support for PinScale / SmartScale models, including diagnostics, calibration and troubleshooting. This course is required if you intend to provide hardware self support. The goal of this training is to make you familiar with: 1. Pinscale / SmartScale tester hardware architecture. 2. Routine system maintenance including running diagnostics and calibration. 3. Identify and repair system start and board failures. 4. Support Documentation. Upon completion of this class you will be capable of performing all the standard troubleshooting and maintenance tasks using the provided troubleshooting tools.

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 PS RF H/W	V93000 Maintenance Training - Port Scale RF	2	請洽教育訓練中心	無	10月06~07日	The goal of this training is to make your familiar with: - PortScale RF tester hardware architecture. - Routine system maintenance including running diagnostics and calibration. - Identify and repair system start and board failures. - Support Documentation. Upon completion of this class you will be capable of performing all the standard troubleshooting and maintenance tasks using the provided troubleshooting tools. <u>Training outline:</u> (1) RF Hardware Elements (2) RF Subsystem Configuration (3) RF Source Card (4) RF Front End Card (5) MB-AV8 Card (6) RF Port naming (7) RF Calibration Hardware (cal kit) (8) Appendix - MB-AV8 Characteristic Data - RF DUT Board Interface - Pogo Mapping - System Specs

SOC Test System – T2000 / T6300 LCD Driver

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T2000	T2000 Basic Programming Training	3	請洽教育訓練中心	無	9月08~10日 12月01~03日	測試程式開發基礎課程 1. 愛德萬測試T2000系統軟硬體架構介紹. 2. Simulator(R3.05)系統操作環境, 學習ATCP測試程式架構, 並能撰寫簡單的測試程式. 課程內容: 1. Hardware and software overview 2. T2000 simulator operation 3. OTPL programming using Advantest Test Class Template Package 4. Test classes code using ATF kit APIs
T6300	T6300 Series (LCD driver IC) TDL Programming Training	5	請洽教育訓練中心	無	8月10~14日 10月19~23日 12月14~18日	先修課程 Pre-requisites: Familiarity with methodologies of digital IC test as well as with Unix system and Text Editors, etc. 適合對象 Target Audience: No experience in testing or T6300 serial. 課程目標 Course Object: This class introduces participants to the T6300 series. Include T6371, T6372 and T6373. Upon completion you will: 1. understand hardware structure of the T6300 series 2. be able to create test programs for LCD driver IC 3. know how to generate and analyze test results 4. know how to debug devices and/or test programs

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T2000 H/W	T2000 Maintenance Training	2	請洽教育訓練中心	無	7月14~15日 10月13~14日	To provide the maintenance engineer with the basic skills and knowledge required to perform basic troubleshooting and repair, and perform required preventive maintenance routines for Advantest's T2000 Test Systems. The participants can completion following after this training: 1. Know HW specification and structure of T2000 2. Know the unit function and signal process of T2000 3. FATAL error troubleshooting 4. Maintenance tasks including diagnostics and calibration

Memory Test System

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T5000 ATL	T5000 Series ATL Programming Elementary Training	4	請洽教育訓練中心	無	7月07~10日	To provide trainees to learn how to start using ADVANTEST memory tester with a simple description of the mechanism, operation and programming. This is achieved through a combination of lectures and lab execises. The training course coverage is # ADVANTEST T5000 memory tester SW/HW overview # Main program/socket program/pattern program introduction in ATL # FutureSuite basic operation # FutureSuite debug/characterization tools # Lab exercises covering simple DC test and function test
T5000 MCI	T5000 Series MCI Programming Basic Training	4	請洽教育訓練中心	無	10月27~30日	Enable the trainees to know the general MCI language skeleton and how to use it to make DC/Function test program under FutureSuite series system. The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. After completion the trainees will be familiar with the following: (1) Key concepts and components of the T5000 series tester. (2) Setup basic test condition with MCI. (3) Be able to develop test program in MCI (4) Calibration, Testflow, Data logging ,Debugging tools and concepts
T5000 HS DDR4	T5833/T5503 HS DDR4 Test Programming Training	4	請洽教育訓練中心	T5000 ATL	8月18~21日	This course introduces T5833/T5503HS software and hardware capability. The trainee learns how to develop solutions for DDR4 using the hardware feature of T5833/T5503HS test system. This is achieved through a combination of lectures and lab execises. The training course coverage is # DDR4 key specification introduction # Test program transfer notice # Sourcesync # Fixed delay edge mode # UBM # Hardware timing training # CRC,DBI # CA parity # Lab exercises covering test solution for DDR4

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T5830 MCI	T5830 MCI Flash Programming Training	4	請洽教育訓練中心	T5000 MCI	11月17~20日	Enable students to create MCI test programs on the T5830 Tester. The intent is to provide the skills required to utilize the T5830 Tester as an integral tool in the engineering and production flows of NAND and NOR device manufacturing. The training described herein serves as an introduction to the functional and operational features. After completion the student will be familiar with the following: (1) Understand key concepts and test system features of the T5830. (2) Be able to develop T5830 test programs in MCI (3) Realize FCM/CFM/BBM/UBM/DBM usage (4) Realize how to generate and analyze test results (5) Realize how to debug devices and/or test programs
T5500 H/W	T5500 Maintenance Training	2	請洽教育訓練中心	無	如果您有此課程需求,請與本教育訓練中心聯絡.	The participants can completion following after this training 1. Know HW specification and structure of T55XX 2. Know the unit function and signal process of T55XX 3. Know the basic device test flow 4. Know the basic maintenance method 5. Know the basic test flow if T55XX
M6242 H/W	M6242 Operation Training	2	請洽教育訓練中心	無	如果您有此課程需求,請與本教育訓練中心聯絡.	The participants can completion following after this training 1. To know Handler Software Screen Configuration and Basic Operations. 2. To Know Handler Operation Start to End 3. To Know Clearing Alarms/ Jam 4. To Know Safety Functions 5. To Know Changing Kit Replacement 6. To Know Backing Up Handler Data

報名注意事項:

1. 報名者請詳填報名表, 並透過 Email方式將報名表寄給教育訓練中心余小姐 dana-hj.yu@advantest.com
   於報名過程中若有任何問題，歡迎電洽本教育訓練中心 (TEL: 03-5975723) 與余小姐聯繫 。
2. 教育訓練中心將於開課日前一週以Email 方式寄發上課通知函給您，請您以電話或Email 方式儘快回覆，以確認開課人數。
3. 若於開課前一週未收到上課確認通知函者，請向本教育訓練中心查詢，以保障您上課的權益。
4. 若您需要取消報名或延後上課，請您務必於開課日前7個工作日主動通知本教育訓練中心，若係臨時取消報名或缺席者，
   報名單位須負擔全額訓練費用。
5. 如無特殊事件發生，所有課程皆如期舉行，本教育訓練中心並保留修訂與取消課程之權利。

ADVANTEST
愛德萬測試股份有限公司
台灣教育訓練中心
聯絡人: 余惠菁 Dana Yu
報名專線: 03-5975723
傳真專線: 03-5981133
Email報名: dana-hj.yu@advantest.com
上課地點: 新竹縣湖口鄉新竹工業區光復路15號