

SOC Test System – V93000

更新日期 2015/11/11

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 I	V93000 SOC Digital User Training	5	請洽 業務代表	無	1月11~15日 2月22~26日 3月07~11日 4月11~15日 5月09~13日 6月20~24日	This class introduces the V93000 SOC Series. After completion you will be familiar with the following: (1) Key concepts and components of the V93000 (2) Setup basic Digital Test Programs (3) Pin configuration setup of Levels, Timing and Vectors (4) Calibration, Testflow, Test Methods, Debugging tools and concepts (5) DC Testing, Shmoo tools, Data logging, Histograms
V93000 MST	V93000 SOC Mixed Signal User Training	4	請洽 業務代表	V93000I	1月25~28日 4月25~28日	After completing this training, the participant will be familiar with the use of instruments to test mixed signal performance parameters and specifications in the V93000 SOC Series. You will know the steps to: (1) Plan appropriate test by utilizing the capabilities and performance of the analog modules of the V93000 SOC test system (2) Program A to D converter and D to A converter in the audio and video frequency range (3) Develop test programs for mixed-signal devices by using test methods (4) Use the available tools for developing and debugging mixed-signal
V93000 PS RF	V93000 SOC Port Scale RF User Training	5	請洽 業務代表	V93000I V93000MST	3月21~25日 5月30~6/3日	The purpose of the V93000 Port Scale training series is to enable students to develop and use RF test programs based on V93000 Port Scale RF test system On completion of the course, participants: (1) know HW specification and structure of PSRF subsystem (2) know how to use software tool to create and measure a CW/Mod signal (3) know how to do RF calibration (4) know how to use RF API in Testmethod (5) know how to do an RF to BB setup & measurement
V93000 Production	V93000 SOC Production Setup Training	2	請洽 業務代表	V93000I	2月02~03日 5月16~17日	The purpose of the V93000 SOC User Training for production setup is to enable students to operate and support production setup activities based on V93000 system. 適合對象: (此為初階課程, 適合新進人員) - Test Engineers - Product Test engineers - Production System Administrator On completion of the course, participants: (1) will be familiar with and know how to setup/configure production test environment including OS, Smartest and Licensing. (2) know how to create or maintain key components for enabling V93000 production including work-order, Prober/Handler driver and Application Model (3) know how to gather and analyze production data, including GDF,STDF,EDF (4) know the Test cell integration solution including Prober/Handler driver interface
V93000 TP	V93000 SOC Test Program Development Training	4	請洽 業務代表	V93000I V93000MST	3月15~18日 6月27~30日	本課程內容針對V93000 testmethod 程式開發讓使用者在短時間學會 C/C++ style Testmethod programming. 所有程式範例可以讓使用者在開發新程式中做參考或複製後稍作修改即可使用。 <u>Lecture: DC Test</u> UTM introduction,93K DC resource overview ,FlexDC introduction , DC set and Mset introduction, sequenced DC introduction, dynamic DC introduction, DC current profile introduction <u>Lecture: Pattern creation</u> Pattern conversion, vector label edit, D2S <u>Lecture: AC Test</u> Clock domain per pin introduction, frequency measurement(DCM), AC parameters measurement by TMU, HW PRBS generator and analyzer Transition tracking, Clock keep alive <u>Lecture: Mixed Signal Test</u> ADC Distortion/Linearity, DAC Distortion/Linearity, SmartCalc(SMC) introduction, Sequencer burst How to creat the library by UTM

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V93000 HSM	V93000 HSM User Training	3	請洽 業務代表	V93000I	4月19~21日	<p>Learn how to set up a GDDR memory test, generate test patterns and analyze test results. After completing this training, you will be able to test memory devices with the V93000 High Speed Memory (HSM) Series.</p> <p>This training is ONLY for engineers new to the HSM Series. Typical responsibilities are the characterization or production testing of memory devices or devices containing embedded memory architectures.</p> <p>(1)HSM Solution (2)High Speed Test (3)HSM GDDR3 Test (4)Generating Memory Test Patterns (5)Executing Memory Test Patterns (6)Debugging Memory Test Patterns (7)Viewing Test Results</p>
V93000 PS H/W	V93000 Maintenance Training - Pin Scale (for SOC & HSM)	3	請洽 業務代表	無	3月29~31日 6月14~16日	<p>This class introduces the principles of V93000 system maintenance and system support for Pin Scale models, including diagnostics, calibration and troubleshooting. This course is required if you intend to provide hardware self support. You will learn how to maintain, calibrate and troubleshoot your V93000 SOC series test system.</p> <p>What you will learn: (1) Understanding the tester hardware architecture (2) Major troubleshooting tools and concept (3) Recommended repair strategies (4) Maintenance tasks including diagnostics and calibration</p>
V93000 PS RF H/W	V93000 Maintenance Training - Port Scale RF	2	請洽 業務代表	無	5月04~05日	<p>Training outline: (1) RF Hardware Elements (2) RF Subsystem Configuration (3) RF Source Card (4) RF Front End Card (5) MB-AV8 Card (6) RF Port naming (7) RF Calibration Hardware (cal kit) (8) Appendix - MB-AV8 Characteristic Data - RF DUT Board Interface - Pogo Mapping - System Specs</p>

SOC Test System – T2000 / T6300 LCD Driver

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T2000	T2000 Basic Programming Training	3	請洽 業務代表	無	3月15~17日 6月14~16日	<p>測試程式開發基礎課程</p> <p>1. 愛德萬測試系統軟硬體架構介紹。 2. 測試原理的基本概論。 3. 學習操作測試系統所提供的功能,能夠撰寫出簡單的測試程式。</p> <p>課程內容:</p> <p>1. Outline 2. Virtual simulator offline operation 3. How to execute a Program 4. Programming exercise 5. DC Parametric Test 6. Functional Test 7. Pattern Program</p>

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T6300	T6300 Series (LCD driver IC) TDL Programming Training	5	請洽 業務代表	無	1月25~29日 3月21~25日 5月23~27日	This class introduces participants to the T6300 series. Include T6371, T6372, T6373 and T6391. Upon completion you will: 1. understand hardware structure of the T6300 series 2. be able to create test programs for LCD driver IC 3. know how to generate and analyze test results 4. know how to debug devices and/or test programs
T2000 H/W	T2000 Maintenance Training	2	請洽 業務代表	無	4月12~13日	The participants can completion following after this training 1. Know HW specification and structure of T2000 2. Know the unit function and signal process of T2000 3. FATAL error troubleshooting 4. Maintenance tasks including diagnostics and calibration

Memory Test System

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T5500 I	T5500/T5300 Series ATL Programming Elementary Course	4	請洽 業務代表	無	2月22~25日	測試程式開發基礎課程 1.愛德萬測試系統軟體架構介紹. 2.測試原理的基本概論. 3.學習測試系統對記憶體測試所提供的功能,能夠撰寫出簡單的測試程式. 課程內容: - Outline - LSI tests and Test Systems - DC Parametric Test - Dynamic Functional Test - Pattern Program - Hi-Fix - Setting for Program Execution and Debugging
T5500 II	T5500/T5300 Series ATL Programming Technique (Application)	3	請洽 業務代表	無	4月26~28日	測試程式開發應用課程 針對特定測試需求,進一步去學習如何使用系統提供的指令集寫出測試程式. 課程內容: 1. RTTC Function 2. FP generator function 3. How to use address fail memory 4. Burst Function 5. Time measurement program 6. Voltage measurement program 7. How to use Z register 8. Area inversion function 9. Flash memory test function 10. Bad block memory function
MRA4	MRA4	2	請洽 業務代表	無	6月01~02日	Memory Repair Analysis 針對記憶體需要修補的部分,去瞭解要如何操作MRA4Tool及使用MRA Program Library. 課程內容: 1. MRA4 hardware and each function feature. 2. MRA4 operation mode introduction. 3. Basic repair algorithm 4. MRA4 tool operation 5. Multistage Decoder mode 6. Main Program creation.
T5500 H/W	T5500 Maintenance Training	2	請洽 業務代表	無	5月10~11日	The participants can completion following after this training 1. Know HW specification and structure of T55XX 2. Know the unit function and signal process of T55XX 3. Know the basic device test flow 4. Know the basic maintenance method 5. Know the basic test flow if T55XX

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M6242 H/W	M6242 Operation Training	2	請洽業務代表	無	3月08~09日	The participants can completion following after this training 1. To know Handler Software Screen Configuration and Basic Operations. 2. To Know Handler Operation Start to End 3. To Know Clearing Alarms/ Jam 4. To Know Safety Functions 5. To Know Changing Kit Replacement 6. To Know Backing Up Handler Data

Cloud Testing Service

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
CX1000	Cloud Testing Service Concept & Operation	1	免費課程	無	如果您有此課程需求,請與本教育訓練中心聯絡	This class introduces concept of CloudTesting Service and operating interface of CTLab software. (1) know concept, and features of Cloud Testing Service (2) be able to test easily by CTS

報名注意事項:

- 報名者請詳填報名表, 並透過 Email 方式將報名表寄給教育訓練中心余小姐 dana-hj.yu@advantest.com
於報名過程中若有任何問題, 歡迎電洽本教育訓練中心 (TEL: 03-5975723) 與余小姐聯繫。
- 教育訓練中心將於開課日前一週以 Email 方式寄發上課通知函給您, 請您以電話或 Email 方式儘快回覆, 以確認開課人數。
- 若於開課前一週未收到上課確認通知函者, 請向本教育訓練中心查詢, 以保障您上課的權益。
- 若您需要取消報名或延後上課, 請您務必於開課日前7個工作日主動通知本教育訓練中心, 若係臨時取消報名或缺席者, 報名單位須負擔全額訓練費用。
- 如無特殊事件發生, 所有課程皆如期舉行, 本教育訓練中心並保留修訂與取消課程之權利。

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台灣教育訓練中心

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