台灣愛德萬教育訓練中心課程表 2017年7月 ~ 12月

SOC Test System – V93000

更新日期 2017/5/4

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
V93000 I	V93000 SOC Digital User Training	5	請洽教育訓練中心	無	6月12~16日 7月10~14日 8月14~16日 9月04~08日 10月16~20日 11月06~10日 12月04~08日	Enable students to create semiconductor test programs on the V93000 SOC Test Platform. The intent is to provide the skills required to utilize the V93000 Tester Platform as an integral tool in the engineering and production flows of semiconductor device manufacturing. The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. After completion the student will be familiar with the following: (1) Key concepts and components of the V93000 (2) Setup basic Digital Test Programs (3) Pin configuration setup of Levels, Timing and Vectors (4) Calibration, Testflow, Test Methods, Debugging tools and concepts (5) DC Testing, Shmoo tools, Data logging, Histograms
V93000 MST	V93000 SOC Mixed Signal User Training	4	請洽教育訓練中心	V93000I	7月24~27日 10月24~27日	After completing this training, the participant will be familiar with the use of instruments to test mixed signal performance parameters and specifications in the V93000 SOC Series. You will know the steps to: (1) Enable students to develop mixed signal device test programs on the V93000 SOC Test Platform. The intent is to provide the skills required to utilize the V93000 Tester Platform as an integral tool in the engineering and production flows of mixed signal device manufacturing. (2) The training described herein serves as an introduction to the functional and operational features and the required user interaction of the system. (3) Develop test programs for mixed-signal devices (4) Use the available tools for developing and debugging mixed-signal
V93000 PS RF	V93000 SOC Port Scale RF User Training	5	請洽教育訓練中心	V93000I V93000MST	8月21~25日 11月20~24日	先修課程 Pre-requisites: The prerequisite for this class is the content of the 93000 Basic User Training class and the 93000 Mixed Signal Training class. It is expected that the students have a good understanding of RF fundamentals prior to attending this class. 適合對象 Target Audience: Test engineers or Product Test engineers who need to develop or support RF Test program on V93000. 課程目標 Course Object: The purpose of the 93000 Port Scale training series is to enable students to develop and use RF test programs based on 93000 Port Scale RF test system On completion of the course, participants: • know HW specification and structure of PSRF subsystem • know how to use software tool to create and measure a CW/Mod signal • know how to do RF calibration • know how to use RF API in Testmethod • know how to do an RF to BB setup & measurement

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V93000 Production	V93000 SOC Production Setup Training	2	請洽教育訓練中心	V93000I	8月30~31日 11月28~29日	The purpose of the V93000 SOC User Training for production setup is to enable students to operate and support production setup activities based on V93000 system. 適合對象: (此為初階課程, 適合新進人員) - Test Engineers - Product Test engineers - Production System Administrator On completion of the course, participants: (1) will be familiar with and know how to setup/configure production test environment including OS, Smartest and Licensing. (2) know how to create or maintain key components for enabling V93000 production including work-order, Prober/Handler driver and Application Model (3) know how to gather and analyze production data, including GDF,STDF,EDF (4) know the Test cell integration solution including Prober/Handler driver interface
V93000 TP	V93000 SOC Test Program Development Training	4	請洽教育訓練中心	V93000I V93000MST	6月20~23日 9月18~21日 12月12~15日	先修課程 Pre-requisites: V93000 Digital User Training, V93000 Mixed Signal Training, Others: C/C++ programming language 適合對象 Target Audience: Test engineer who needs to develop SOC or high speed test program 課程目標 Course Object: This course focus on test program development by using C/C++ style testmethod coding.The purpose of this course is to demonstrate the C/C++ example code by using testmethod API and standard C/C++ library, which includes DC Test, pattern creation, AC Test, high speed Test and Mixed-Signal Test. The TPD2.0 mainly emphasizes the PS1600 feature(sequence DC, TMU, high speed pattern sync and tracking).The TPD2.0 has to add the pattern conversion. So the TPD2.0 course can supply the whole 93k test program development from basic to mixed signal skill. (SMT Version: 7.2.2.4) <u>Lecture: DC Test</u> UTM introducion,93K DC resource overview ,FlexDC introduction ,DC set and Mset introduction, sequenced DC introduction, dynamic DC introduction, DC current profile introduction <u>Lecture: Pattern creation</u> Pattern conversion, vector label edit, D2S <u>Lecture: AC Test</u> Clock domain per pin introduction, frequency measurement(DCM), AC parameters measurement by TMU, HW PRBS generator and analyzer, Transition tracking, Clock keep alive <u>Lecture: Mixed Signal Test</u> ADC Distortion/Linearity, DAC Distortion/Linearity, SmartCalc(SMC) introduction, Sequencer burst, How to creat the library by UTM
V93000 PS H/W	V93000 Maintenance Training - Pin Scale (for SOC & HSM)	3	請洽教育訓練中心	無	6月26~28日 9月26~28日 12月19~21日	This class introduces the principles of V93000 system maintenance and system support for PinScale / SmartScale models, including diagnostics, calibration and troubleshooting. This course is required if you intend to provide hardware self support. The goal of this training is to make you familiar with: 1. Pinscale / SmartScale tester hardware architecture. 2. Routine system maintenance including running diagnostics and calibration. 3. Identify and repair system start and board failures. 4. Support Documentation. Upon completion of this class you will be capable of performing all the standard troubleshooting and maintenance tasks using the provided troubleshooting tools.

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V93000 PS RF H/W	V93000 Maintenance Training - Port Scale RF	2	請洽教育訓練中心	無	11月14~15日	The goal of this training is to make your familiar with: - PortScale RF tester hardware architecture. - Routine system maintenance including running diagnostics and calibration. - Identify and repair system start and board failures. - Support Documentation. Upon completion of this class you will be capable of performing all the standard troubleshooting and maintenance tasks using the provided troubleshooting tools. <u>Training outline:</u> (1) RF Hardware Elements (2) RF Subsystem Configuration (3) RF Source Card (4) RF Front End Card (5) MB-AV8 Card (6) RF Port naming (7) RF Calibration Hardware (cal kit) (8) Appendix - MB-AV8 Characteristic Data - RF DUT Board Interface - Pogo Mapping - System Specs

SOC Test System – T2000 / T6300 LCD Driver

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T2000	T2000 Basic Programming Training	3	請洽教育訓練中心	無	6月13~15日 9月05~07日 12月05~07日	測試程式開發基礎課程 1. 愛德萬測試T2000系統軟硬體架構介紹. 2. Simulator(R3.05)系統操作環境, 學習ATCP測試程式架構, 並能撰寫簡單的測試程式. 課程內容: 1. Hardware and software overview 2. T2000 simulator operation 3. OTPL programming using Advantest Test Class Template Package 4. Test classes code using ATF kit APIs
T6300	T6300 Series (LCD driver IC) TDL Programming Training	5	請洽教育訓練中心	無	7月10~14日 9月18~22日 11月06~10日	先修課程 Pre-requisites: Familiarity with methodologies of digital IC test as well as with Unix system and Text Editors, etc. 適合對象 Target Audience: No experience in testing or T6300 serial. 課程目標 Course Object: This class introduces participants to the T6300 series. Include T6371, T6372 and T6373. Upon completion you will: 1. understand hardware structure of the T6300 series 2. be able to create test programs for LCD driver IC 3. know how to generate and analyze test results 4. know how to debug devices and/or test programs

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T2000 H/W	T2000 Maintenance Training	2	請洽教育訓練中心	無	10月24~25日	The participants can completion following after this training: 1. Know HW specification and structure of T2000 2. Know the unit function and signal process of T2000 3. FATAL error troubleshooting 4. Maintenance tasks including diagnostics and calibration

Memory Test System

課程代號	課程名稱	天數	訓練費用	先修課程	開課日期	課程簡介
T5500 I	T5500/T5300 Series ATL Programming Elementary Course	4	請洽教育訓練中心	無	8月14~17日	測試程式開發基礎課程 1.愛德萬測試系統軟硬體架構介紹. 2.測試原理的基本概論. 3.學習測試系統對記憶體測試所提供的功能,能夠撰寫出簡單的測試程式. 課程內容: - Outline - LSI tests and Test Systems - DC Parametric Test - Dynamic Functional Test - Pattern Program - Hi-Fix - Setting for Program Execution and Debugging
T5500 II	T5500/T5300 Series ATL Programming Technique (Application)	3	請洽教育訓練中心	無	10月17~19日	測試程式開發應用課程, 針對特定測試需求, 進一步去學習如何使用系統提供的指令集寫出測試程式. 課程內容: 1. RTTC Function 2. FP generator function 3. How to use address fail memory 4. Burst Function 5. Time measurement program 6. Voltage measurement program 7. How to use Z register 8. Area inversion function 9. Flash memory test function 10. Bad block memory function
MRA4	MRA4	2	請洽教育訓練中心	無	12月12~13日	Memory Repair Analysis 針對記憶體需要修補的部分,去瞭解要如何操作MRA4Tool及使用MRA Program Library. 課程內容: 1. MRA4 hardware and each function feature. 2. MRA4 operation mode introduction. 3. Basic repair algorithm 4. MRA4 tool operation 5. Multistage Decoder mode 6. Main Program creation.

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T5500 H/W	T5500 Maintenance Training	2	請洽教育訓練中心	無	11月14~15日	The participants can completion following after this training 1. Know HW specification and structure of T55XX 2. Know the unit function and signal process of T55XX 3. Know the basic device test flow 4. Know the basic maintenance method 5. Know the basic test flow if T55XX
M6242 H/W	M6242 Operation Training	2	請洽教育訓練中心	無	9月26~27日	The participants can completion following after this training 1. To know Handler Software Screen Configuration and Basic Operations. 2. To Know Handler Operation Start to End 3. To Know Clearing Alarms/ Jam 4. To Know Safety Functions 5. To Know Changing Kit Replacement 6. To Know Backing Up Handler Data

報名注意事項:

1. 報名者請詳填報名表, 並透過 Email方式將報名表寄給教育訓練中心余小姐 dana-hj.yu@advantest.com
   於報名過程中若有任何問題，歡迎電洽本教育訓練中心 (TEL: 03-5975723) 與余小姐聯繫 。
2. 教育訓練中心將於開課日前一週以Email 方式寄發上課通知函給您，請您以電話或Email 方式儘快回覆，以確認開課人數。
3. 若於開課前一週未收到上課確認通知函者，請向本教育訓練中心查詢，以保障您上課的權益。
4. 若您需要取消報名或延後上課，請您務必於開課日前7個工作日主動通知本教育訓練中心，若係臨時取消報名或缺席者，
   報名單位須負擔全額訓練費用。
5. 如無特殊事件發生，所有課程皆如期舉行，本教育訓練中心並保留修訂與取消課程之權利。

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