ADVANTEST

Memory Test System

T5503

256-DUT Parallel Test Capacity for Mass Production Test of High Speed DDR3-SDRAM Memory Devices

ADVANTEST

Broadband communications are now widely used in households and businesses throughout the globe and expectations are growing for higher quality and larger capacity applications including video, audio, and on-line content. The demand for low-power, very high-speed memory to drive these applications is prompting the near-term transition from DDR2-SDRAM to DDR3-SDRAM. DDR3's increased performance, coupled with lower power requirements, promise to measurably enhance the capabilities of equipment such as PCs, notebooks and servers, as well as advanced consumer products such as game consoles and HDTV.

Accompanying the shift from DDR2-SDRAM to the faster DDR3-SDRAM, is a corresponding requirement from device manufacturers for higher-speed and more accurate test capabilities, and an insistence on lowered test costs for mass production of these new high-speed devices. Advantest's T5503 8448 Channel Test Head addresses these needs, with superior throughput and lowered test costs for high-speed manufacturing. When coupled with Advantest's M6242 handler, the T5503 provides a DDR3 test cell that delivers performance and yield as well as the industry's lowest cost of volume production test.

Up to 3.2Gbps of test speed and 256 device parallel test

For package test of the new-generation high-speed memory DDR3-SDRAM, the T5503 achieves the fastest test speeds in the industry of 3.2Gbps.
while delivering parallel test of up to 256 devices (four times that of its predecessors). This contributes to a major reduction in test costs for high volume production lines and makes it an ideal solution for DDR3-SDRAM, as well as for GDDR3 and GDDR4.

In addition, the T5503 provides an enriched multi-strobe function to measure the phase difference between the reference clock signal and data output signals at each cycle, -- a significant benefit as the operating frequencies becomes higher. This function helps to measure the characteristics of memory devices at high speeds with a high level of accuracy.

Reduced power requirements and a space savings of 30%

By adopting the latest CMOS technology, a high degree of integration has been achieved in the semiconductor circuits mounted on the tester. While realizing four times the parallel test capacity of the earlier version, the new system configuration has a 30% smaller footprint, contributing to an energy-efficient and space-saving production line environment.

Using FutureSuite®, with multi-language support

Advantest's proprietary FutureSuite software with multi-language support enhances the system's operating system. In addition, the conventional ATL language, the MCI (Multi Control Interface) language allows programming in C.

T5503

T5503 Key Specifications

Target Devices:	**DDR3-SDRAM, GDDR3/4-SDRAM, etc.**
Parallel Testing:	**Up to 256 devices per system**
Test Speed:	**Up to 3.2Gbps**

*FutureSuite is a registered trademark of Advantest in Japan, US, and other countries.

Please refer to product manual for complete system specifications.
Specifications may change without notification.

ADVANTEST®

http://www.advantest.co.jp

ADVANTEST CORPORATION
Shin-Marunouchi Center Building,
1-6-2 Marunouchi, Chiyoda-ku,
Tokyo 100-0005, Japan
Phone: +81-3-3214-7500
Fax: +81-3-3214-7705

Advantest America, Inc.
3201 Scott Boulevard,
Santa Clara, CA 95054, U.S.A.
Phone: +1-408-988-7700
Fax: +1-408-987-0691

Advantest Europe GmbH
Stefan-George-Ring 2,
D-81929 Munich, Germany
Phone: +49-89-99312-0
Fax: +49-89-99312-101

Advantest (Singapore) Pte. Ltd.
6 Serangoon North Avenue 5,
#06-03, Singapore 554910,
Singapore
Phone: +65-6752-5990
Fax: +65-6752-3910

Advantest Taiwan Inc.
No.1, Alley 17, Lane 62, Zhonghe St.,
Zhubei City, Hsinchu County 302,
Taiwan R.O.C.
Phone: +886-3-5532111
Fax: +886-3-5541168

Advantest Korea Co., Ltd.
22BF, Kyobo KangNam Tower,
1303-22, Seocho-Dong, Seocho-Ku,
Seoul #137-070, Korea
Phone: +82-2-3478-9400
Fax: +82-2-532-7132

Advantest (Suzhou) Co., Ltd.
17A, Suzhou International Science Park,
No.1355 Jinjihu Avenue,
Suzhou Industrial Park, Suzhou,
Jiangsu, China 215021
Phone: +86-512-6256-8318
Fax: +86-512-6256-8328

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