

ADVANTEST®

Analog Test System

T7910

The industry's most efficient analog test system for power ICs



The demand for highly efficient power supplies is increasing as mobile devices and electronic devices such as OA peripheral equipment become smaller and lighter in weight.

The new, T7910, is an analog test system specifically designed for testing power supply ICs. It offers greater functionality and performance than the earlier model T7912, and is capable of testing power ICs with fewer pins with higher efficiency.

An optimum channel configuration can be selected to match the number of pins and devices for performing parallel tests, resulting in higher performance and an overall reduction in test costs.

Optimized channel configuration

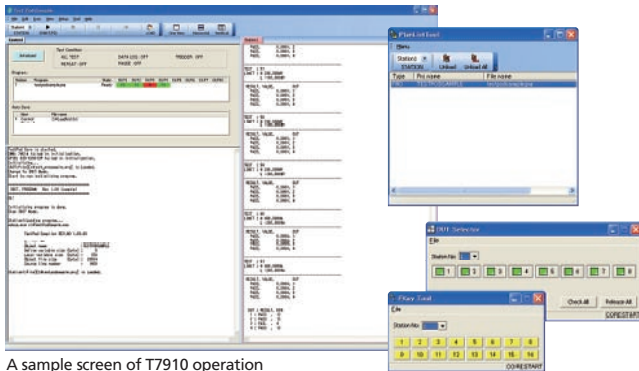
The optimal channel configuration can be selected with 2 to 16 channels for device power supply and 6 to 48 channels for I/O power supply, to match the number of pins and devices for performing parallel tests.

System software

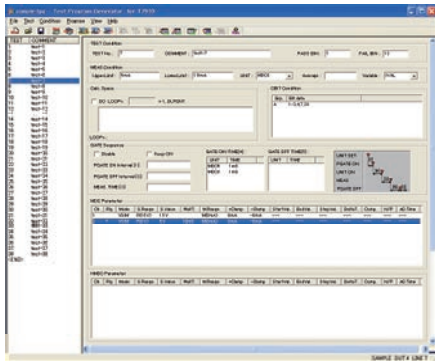
- Functions can be extended using COM
- Fast test data collection in the log Customizable log output format
- Device program generation tool is available

Key specification of T7910

Target Devices:	Low-pin-count power ICs
Parallel test capacity:	Up to 8
Basic configuration	
Device power supply:	from 2 to 16 channels ±64 V/±300 mA to 8 μ A ±32 V/±500 mA to 8 μ A, ±32 V/±1 A (pulse) ±16 V/±500 mA to 8 μ A, ±16 V/±3 A (pulse)
I/O power supply:	6 to 48 channels ±64 V/±32 mA to 8 μ A, ±16 V/±64 mA to 8 μ A
Optional	
High voltage power supply:	1 to 8 channels (MPX switchover) +2 kV/+1 mA
Time measurement unit:	8 channels (A/B × 4 channels)
Measurement range:	10 ns to 65 s, 100 ps resolution



A sample screen of T7910 operation



A sample screen of device program generation tool



Please refer to product manual for complete system specifications.
Specifications may change without notification.

ADVANTEST®

<http://www.advantest.co.jp>

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