

## Question: "How do you execute GDDR-5 device training during a shmoo run?" Dong-Myong Kim

## Question:

Due to high speeds, GDDR-5 devices require adjusting timings through device training steps. These timing adjustment values are dependent on device settings like VDD, frequency etc. When shmooing along such a parameter the device training needs to be executed repetitively during the shmoo run. The shmoo supplied with smartest do not allow this, so how can this be done?

## Answer:

Shmoo's are an important tool for customers to characterize new devices. A tAC vs. Frequency or VDD vs Frequency shmoo are standard shmoos to judge the device performance. Due to ever increasing speeds more and more devices, like the GDDR-5 and DDR-4, contain device training steps to adjust the timings at runtime. When running a shmoo the necessity to execute the device training during the shmoo run arises.

For the HSM community an enhanced shmoo tool "MS Shmoo Plus" has been developed, that allows the integration of the device training into the shmoo execution.

The "MS Shmoo Plus" defines three hook functions that are called before, during the shmoo loop and after the shmoo execution. These functions are contained in a standalone shared library, allowing to switch or modify the additional functionality without the need to touch the "MS Shmoo Tool" code.





For GDDR-5 devices these functions are used as follows. During the PreExecuting the current values of the shmoo parameter settings is stored. These values are restored after the shmoo execution. During the Execution function call it is checked whether a device training is required and if so, it is executed.

For a tAC vs Frequency shmoo it is necessary to run the device training, every time the frequency is changed. Thus the training is executed after each complete shmoo line.



For a VDD vs. Frequency shmoo the training must be executed when either the supply voltage or the frequency is changed, thus after each shmoo point setting.



As the examples show the functionality to be executed depends on the shmoo parameters. Therefore multiple Execute functions should be accessible. The MS Shmoo Plus allows to define a "infinite" number of functions and select them directly from the test method's user interface.



	$\nabla$	result		
		resultPins	DQ_Bus[7:0],DQ_Bus[15:8],DQ23_Pin,DQ21_Pin,DQ20_Pi	n,E
		perPinResult	Disable	
		testMode	pass/fail	
		Disconnect/Con	Disable	
		User Callback Fu	training_X_DQtiming_Y_Level	٣
	v	Output	None	-
		GraphicalOuts	emptyUserfunc	
Limits		imits .	training_X_DQtiming_Y_Level	=
Þ	Flags		training_X_DQtiming_Y_DevicePeriod	
r			training X Level Y DevicePeriod	
P	Site Control			1

The "MS Shmoo Plus" is currently used at customer site to support the GDDR-5 characterization. Due to its flexible architecture the tool natively supports all devices, that require the execution of device specific functionality during the shmoo execution run.